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APPLICATION INFORMATION

Title Line One:: BIPOLAR TRANSISTOR AND FABRICATION METHO

Title Line Two:: D THEREOF Total Drawing Sheets:: 13 Formal Drawings?:: Yes Application Type:: Utility Docket Number:: 740819-734

Secrecy Order in Parent Appl.?:: No

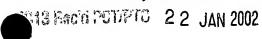
REPRESENTATIVE INFORMATION

Representative Customer Number:: 22204

Registration Number One:: 38285 Registration Number Two:: 28290 Registration Number Three:: 35483

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Registration Number Four:: 45285 Registration Number Five:: 26477



CONTINUITY INFORMATION

This application is a:: 371 OF

> Application One:: PCT/JP01/04344

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PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2000-151044

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